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## Challenges in accelerating Graph Machine Learning



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**Energy-Aware Scheduling for  
Serverless Scientific Workflows:  
A Machine Learning Approach**



Prof. Guido Araujo (UNICAMP)

# Source-based Matching and Rewriting



## Source-based Matching and Rewriting

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## Dynamic Hardware Customisation for Mobile Users in FPGA-accelerated Edge Infrastructures



Diogo Gonçalves and  
Prof. Alfredo Goldman (IME USP)

# Pedro Henrique Di Francia Rosso (PhD student, UNICAMP) Integrating FPGA Acceleration to OpenMP Distributed Computing



Pedro Rosso and Prof. Alfredo Goldman (IME USP)

Prof. Raphael  
Yokoingawa de  
Camargo (UFABC)

## Machine Learning for HPC Scheduling

